

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Shigeru ONOYA

Art Unit : Unknown

Serial No. : Not yet assigned

Examiner : Unknown

Filed : February 8, 2001

Title : SEMICONDUCTOR DEVICE AND METHOD OF DRIVING
SEMICONDUCTOR DEVICE

Commissioner for Patents
Washington, D.C. 20231

8-6-0
Prof. [REDACTED] (NE)
Not Signed

PRELIMINARY AMENDMENT

Prior to examination, please amend the application as follows:

5. (Amended) A method of driving a semiconductor display device according to claim 1, [any of claims 1 to 4,] wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.

6. (Amended) A method of driving a semiconductor display device according to claim 1, [any of claims 1 to 4,] wherein the polarity of the display signals inputted to all of the pixel electrodes may be inverted in two adjacent frame periods.

11. (Amended) A semiconductor display device according to [any of claims 7 to 10,] claim 7, wherein the polarity data [is] comprises information regarding the polarity of the display signals inputted to all of the pixels.

Please add claims 15-23 as follows:

--15. A method of driving a semiconductor display device according to claim 2, wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.--

--16. A method of driving a semiconductor display device according to claim 3, wherein pixel electrodes to which the display signals having positive polarity are inputted, and pixel electrodes to which the display signals having negative polarity are inputted may differ randomly every frame period.--